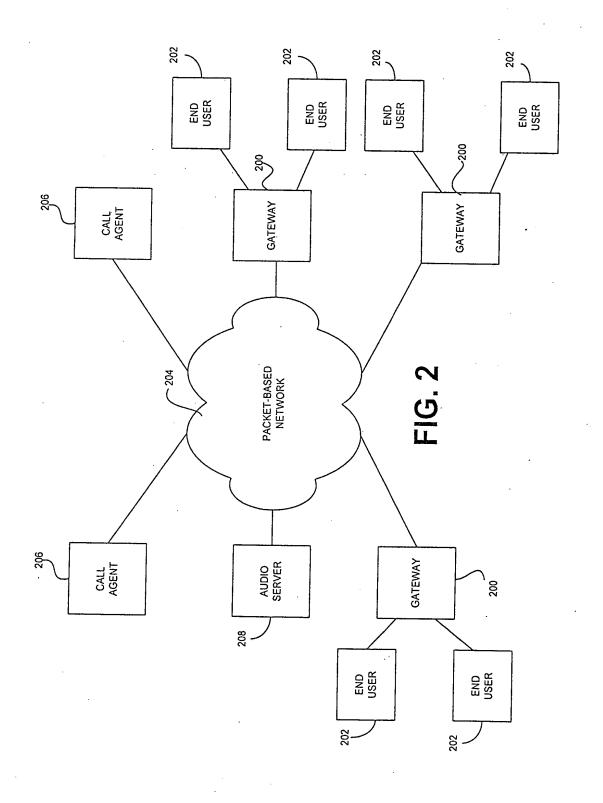


Y



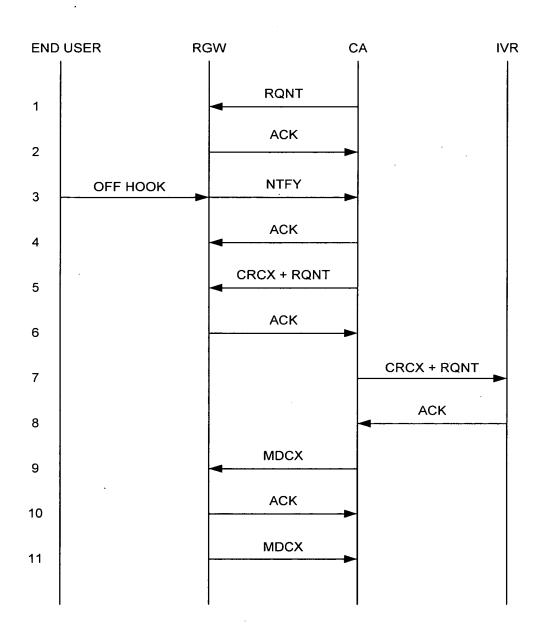


FIG. 3

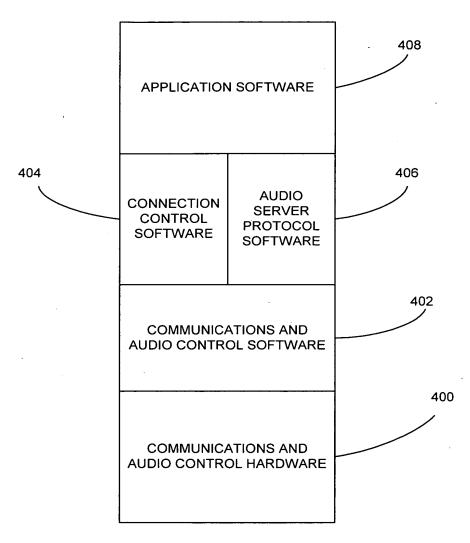


FIG. 4

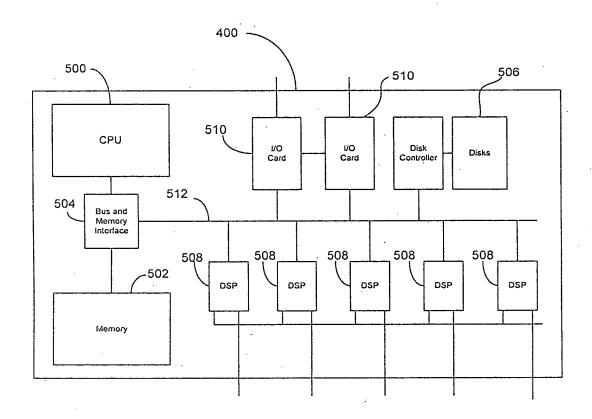


FIG. 4(a)

SEQUENCE PROCESSOR

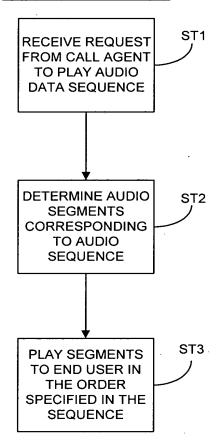


FIG. 5

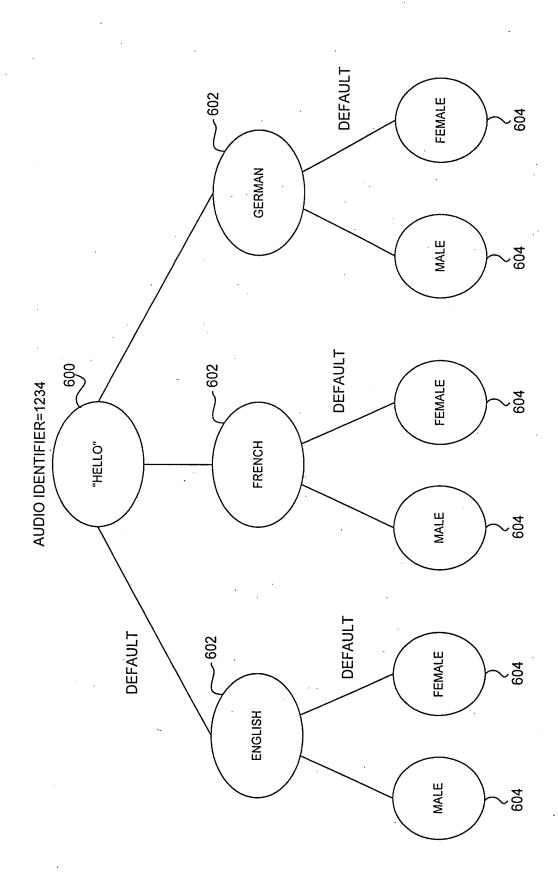


FIG. 6

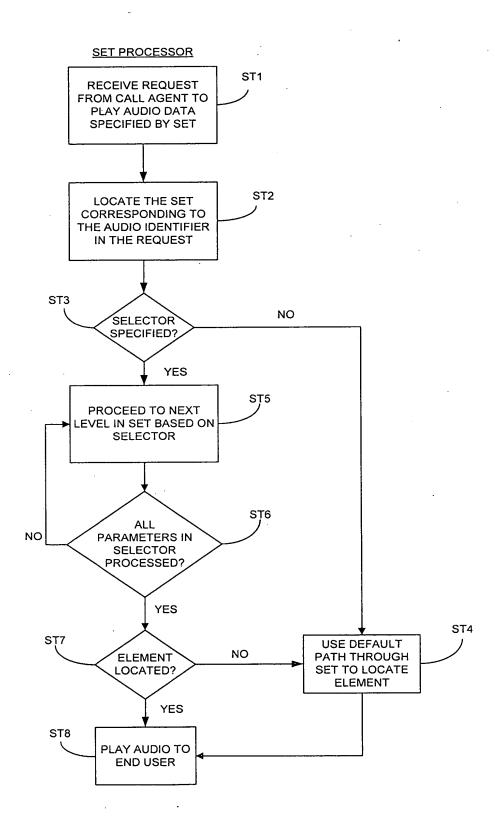


FIG. 7

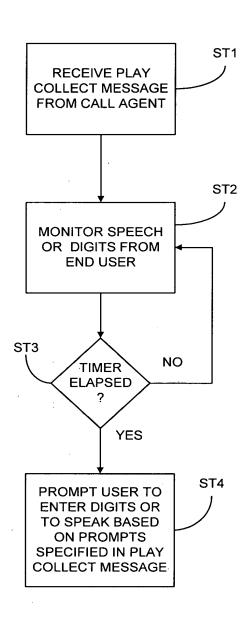


FIG. 8

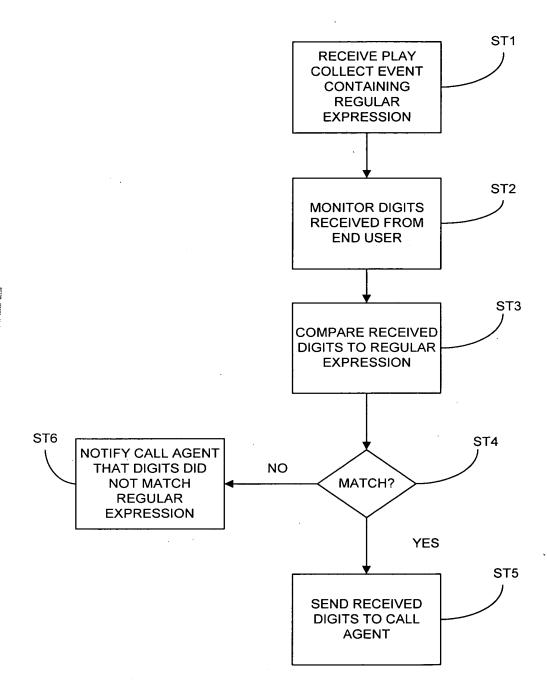
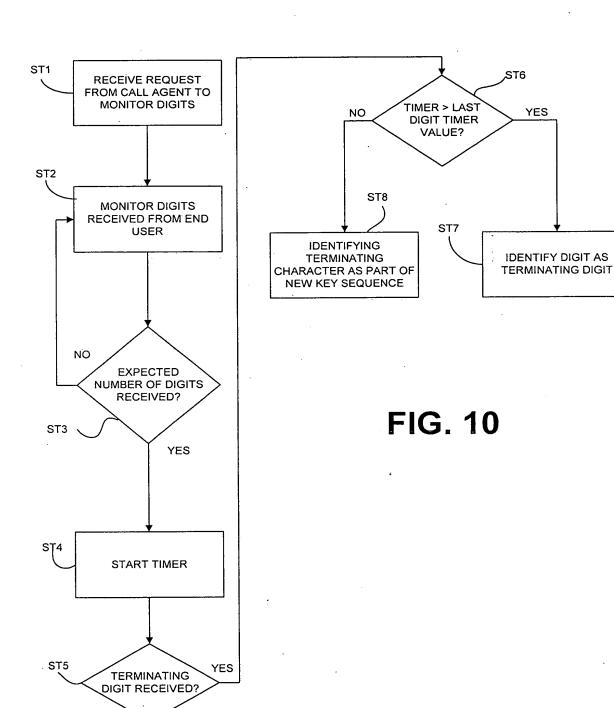


FIG. 9



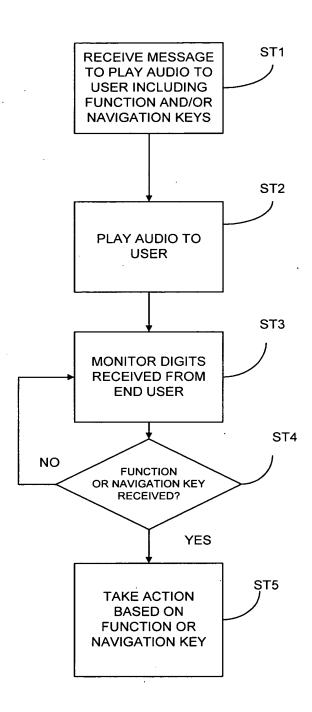


FIG. 11

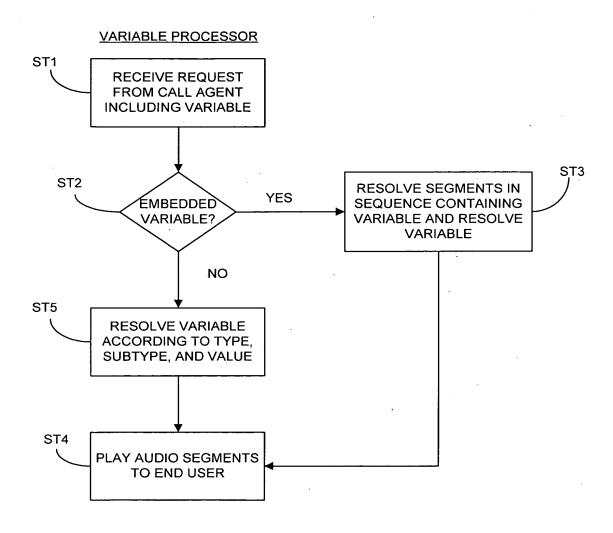


FIG.12